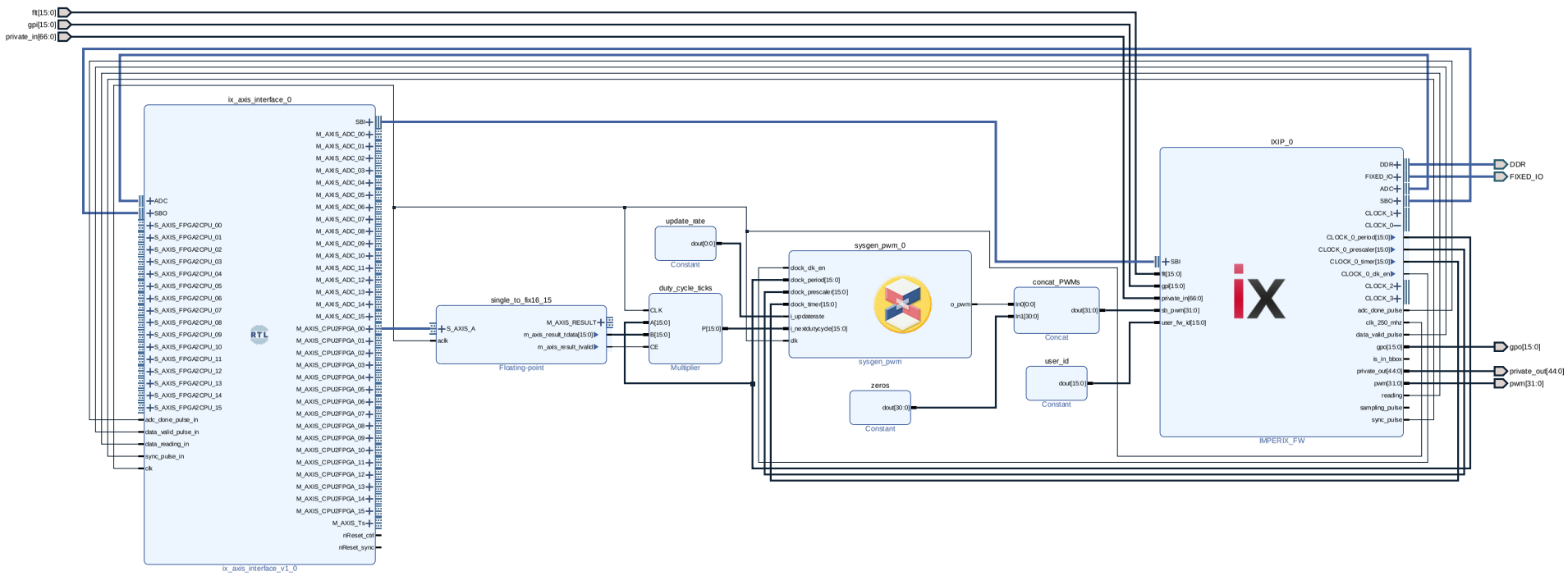
flt[15:0]
gpi[15:0]
private_in[66:0]
ix_axis_interface_0
SBI
M_AXIS_ADC_00
M_AXIS_ADC_01
M_AXIS_ADC_02
M_AXIS_ADC_03
M_AXIS_ADC_04
M_AXIS_ADC_05
M_AXIS_ADC_06
M_AXIS_ADC_07
M_AXIS_ADC_08
M_AXIS_ADC_09
M_AXIS_ADC_10
M_AXIS_ADC_11
M_AXIS_ADC_12
M_AXIS_ADC_13
M_AXIS_ADC_14
M_AXIS_ADC_15
M_AXIS_CPU2FPGA_00
M_AXIS_CPU2FPGA_01
M_AXIS_CPU2FPGA_02
M_AXIS_CPU2FPGA_03
M_AXIS_CPU2FPGA_04
M_AXIS_CPU2FPGA_05
M_AXIS_CPU2FPGA_06
M_AXIS_CPU2FPGA_07
M_AXIS_CPU2FPGA_08
M_AXIS_CPU2FPGA_09
M_AXIS_CPU2FPGA_10
M_AXIS_CPU2FPGA_11
M_AXIS_CPU2FPGA_12
M_AXIS_CPU2FPGA_13
M_AXIS_CPU2FPGA_14
M_AXIS_CPU2FPGA_15
M_AXIS_Ts
nReset_ctrl
nReset_sync
ADC
SBO
S_AXIS_FPGA2CPU_00
S_AXIS_FPGA2CPU_01
S_AXIS_FPGA2CPU_02
S_AXIS_FPGA2CPU_03
S_AXIS_FPGA2CPU_04
S_AXIS_FPGA2CPU_05
S_AXIS_FPGA2CPU_06
S_AXIS_FPGA2CPU_07
S_AXIS_FPGA2CPU_08
S_AXIS_FPGA2CPU_09
S_AXIS_FPGA2CPU_10
S_AXIS_FPGA2CPU_11
S_AXIS_FPGA2CPU_12
S_AXIS_FPGA2CPU_13
S_AXIS_FPGA2CPU_14
S_AXIS_FPGA2CPU_15
adc_done_pulse_in
data_valid_pulse_in
data_reading_in
sync_pulse_in
clk
RTL
ix_axis_interface_v1_0
single_to_fix16_15
S_AXIS_A
aclk
M_AXIS_RESULT
m_axis_result_tdata[15:0]
m_axis_result_tvalid
Floating-point
update_rate
dout[0:0]
Constant
duty_cycle_ticks
CLK
A[15:0]
B[15:0]
CE
P[15:0]
Multiplier
sysgen_pwm_0
clock_clk_en
clock_period[15:0]
clock_prescaler[15:0]
clock_timer[15:0]
i_updateratio
i_nextdutycycle[15:0]
clk
o_pwm
sysgen_pwm
zeros
dout[30:0]
Constant
concat_PWMs
in0[0:0]
in1[30:0]
dout[31:0]
Concat
user_id
dout[15:0]
Constant
IXIP_0
SBI
flt[15:0]
gpi[15:0]
private_in[66:0]
sb_pwm[31:0]
user_fw_id[15:0]
DDR
FIXED_IO
ADC
SBO
CLOCK_1
CLOCK_0
CLOCK_0_period[15:0]
CLOCK_0_prescaler[15:0]
CLOCK_0_timer[15:0]
CLOCK_0_clk_en
CLOCK_2
CLOCK_3
adc_done_pulse
clk_250_mhz
data_valid_pulse
gpo[15:0]
is_in_bbox
private_out[44:0]
pwm[31:0]
reading
sampling_pulse
sync_pulse
IMPERIX_FW
DDR
FIXED_IO
gpo[15:0]
private_out[44:0]
pwm[31:0]